1uS = 20x50nS = 80x12.5nS
left card shift register
shift register
shift register
shift register
shift register
shift register
shift register
shift register
shift register
shift register
shift register
shift register
shift register
shift register
shift register
shift register
right card shift register
en
en
en
en
en
crude track detection (output pattern number(s))
crude track detection (output pattern number(s))
crude track detection (output pattern number(s))
crude track detection (output pattern number(s))
crude track detection (output pattern number(s))
5 in OR array
OR Binning - Kappa 4 bits, Phi 2 bits, duration 4 bits ?
Maximum 5 results per 20MHz clock using encoded system.
5 crude segment detections